SERIAL NO.: 10/606,971

EXAMINER: Felix E. Suarez

ART UNIT: 2857

Page 2 of 13

## **Amendments to the Specification**

Please replace the paragraph found on page 5, line 28 – page 6, line 3 with the following amended paragraph:

With continued reference to Figure 5, the pin electronics circuitry 60 comprises high-speed drivers 62 and receivers 64 to provide a communication interface between the tester and the DUT 40. The present invention lends itself well to flexible channel architectures such that single-ended and differential drivers and receivers may be employed. Such constructions are well-known to those skilled in the art. A preferred pin electronics channel architecture that advantageously provides a flexible AC/DC coupled environment between the tester 30 and the DUT 40 is described in co-pending U.S. Patent Application Serial Number [[\_\_\_\_\_\_, ]] 10/404,900, titled Hybrid AC/DC Coupled Channel For Automatic Test Equipment, filed March 31, 2003, assigned to the assignee of the present invention, and expressly incorporated herein by reference.